

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A liquid crystal display device, comprising:

a first substrate;

a plurality of gate lines formed on said first substrate;

a plurality of data lines, electrically insulated from said plurality of gate lines, formed on said first substrate such that said plurality of data lines intersect with said plurality of gate lines to define cell areas;

a pixel electrode formed in each of said cell areas;

a transistor associated with each pixel electrode, said transistor having a gate, source and drain, said drain connected to said pixel electrode, said source connected to one of said plurality of data lines, and said gate connected to one of said plurality of gate lines;

barrier ribs formed on said source and drain of each transistor;

an ink-jet color filter formed in each cell area between said barrier ribs; **and**

a protective layer formed in a portion of each cell ~~area; and~~

area, wherein

~~a portion of~~ said ink-jet color filter formed in each cell area is formed on said protective layer.

2. (Original) The liquid crystal display device of claim 1, wherein said barrier ribs are a photo-resist pattern.

3. (Original) The liquid crystal display device of claim 1, wherein at least one of said barrier ribs formed on one of said drains has a contact hole formed therein to expose said drain; and
said pixel electrode associated with said transistor having said exposed drain is connected to said exposed drain via said contact hole.

4. (Original) The liquid crystal display device of claim 1, wherein said barrier ribs prevent light from leaking there through.

5. (Cancelled)

6. (Original) The liquid crystal display device of claim 1, wherein said pixel electrode in each cell area is formed on said ink-jet color filter in said cell area.

7. (Original) The liquid crystal display device of claim 1, wherein said barrier ribs have a stripe-shape in a direction parallel to said plurality of data lines.

8. (Currently Amended) The liquid crystal display device of claim 7, wherein

said barrier ribs adjacent to one another and associated with different transistors define a column area, said column area including a plurality cell areas; and

each said ink-jet color **area filter is** formed in one of said column areas.

9. (Original) The liquid crystal display device of claim 1, further comprising:

a second substrate opposed to said first substrate; and

liquid crystal disposed between said first and second substrates.

10. (Currently Amended) A liquid crystal display device, comprising:

a first substrate;

barrier lines formed in a ~~first~~ single direction over said first substrate, adjacent barrier lines defining a column area; and
an ink-jet color filter formed in at least one of said column areas.

11. (Original) The liquid crystal display device of claim 10, wherein said barrier lines are a photo-resist pattern.

12. (Original) The liquid crystal display device of claim 10, wherein said barrier lines prevent light from leaking there through.

13. (Currently Amended) The liquid crystal display device of claim 10, further comprising:

a plurality of gate lines formed on said first substrate;

a plurality of data lines, electrically insulated from said plurality of gate ~~line,~~ lines formed on said ~~second~~ first substrate such that said plurality of data lines intersect with said plurality of gate lines to define cell areas;

a pixel electrode formed in each of said cell areas; and

a transistor associated with each pixel electrode, said transistor having a gate, source and drain, said drain connected to

said pixel electrode, said source connected to one of said plurality of data lines, and said gate connected to one of said plurality of gate lines.

14. (Original) The liquid crystal display device of claim 13, wherein said barrier lines are formed parallel to said plurality of data lines.

15. (Original) The liquid crystal display device of claim 13, wherein said barrier lines are formed on said sources and drains of said transistors.

16. (Original) The liquid crystal display device of claim 14, wherein at least one of said barrier lines formed on one of said drains has a contact hole formed therein to expose said drain; and
said pixel electrode associated with said transistor having said exposed drain is connected to said exposed drain via, said contact hole.

17. (Original) The liquid crystal display device of claim 14, further comprising:

a protective layer formed in a portion of each cell area; and

wherein

a portion of said ink-jet color filter formed in each cell area is formed on said protective layer.

18. (Original) The liquid crystal display device of claim 14, wherein said pixel electrode in each cell area is formed on said ink-jet color filter in said cell area.

19. (Currently Amended) The liquid crystal display device of claim 10, further comprising:

a second substrate opposed to said first substrate;

a plurality of gate lines formed on said second substrate;

a plurality of data lines, electrically insulated from, said plurality of gate ~~line~~, lines, formed on said second substrate such that said plurality of data lines intersect with said plurality of gate lines to define cell areas;

a pixel electrode formed in each of said cell areas; and

a transistor associated with each pixel electrode, said transistor having a gate, source and drain, said drain connected to said pixel electrode, said source connected to one of said plurality of data lines, and said gate connected to one of said plurality of gate lines.

20. (Original) The liquid crystal display device of claim 19, wherein each of said barrier lines overlaps one of said plurality of data lines.

21. (Original) The liquid crystal display device of claim 19, wherein each of said barrier lines overlaps one of said plurality of gate lines.

22. (Original) The liquid crystal display device of claim 19, further comprising:

a low reflective layer formed on at least one of said plurality of data and gate lines.

23. (Original) A method of fabricating a liquid crystal display device, comprising:

forming gate lines and at least one gate electrode connected to one of said gate lines on a substrate;

forming an insulation layer over said substrate;

forming an active layer over said gate electrode;

forming a metal layer over said substrate;

forming barrier lines on said metal layer;

patterning said metal layer using said barrier lines as a mask to form source and drain electrodes on said active region and to form data lines;

forming a color filter between at least two adjacent barrier lines using ink ejection; and

forming a pixel electrode connected to said drain electrode.

24. (Original) A method of fabricating a liquid crystal display device, comprising:

forming a first insulation layer on a substrate;

forming an active layer on said insulation layer;

forming a second insulation layer over said substrate;

forming gate lines and a gate electrode, connected to one of said gate lines and disposed over said active layer, on said second insulation layer;

forming a protective film over said substrate;

forming source and drain electrodes, in electrical contact with source and drain regions of said active layer, on said protective film; and

forming barrier lines on said source and drain electrodes;

forming a color filter between at least two adjacent barrier lines using ink ejection;

removing said barrier lines;
forming a pixel electrode connected to said drain electrode;
and
forming light shielding lines over said source and drain electrodes.

25. (Currently Amended) A method of fabricating a liquid crystal display device, comprising:

forming a transistor structure on a substrate, said transistor structure including source and drain electrodes connected to an active region; **and**

forming barrier lines on said source and drain electrodes; **and**
forming a color filter between at least two adjacent barrier lines using ink ejection; and

forming a pixel electrode on the color filter and connected to said drain electrode.

26. (Currently Amended) A method of fabricating a liquid crystal display device, comprising:

forming a plurality of barrier lines **in a single direction** on a first substrate; and

forming a color filter between at least two adjacent barrier lines using ink ejection.

27. (New) The liquid crystal display device of claim 1, further comprising:

a gate insulation film on the first substrate,
wherein the protective layer is disposed between the gate insulation film and the ink-jet color filter.

28. (New) The liquid crystal display device of claim 17, further comprising:

a gate insulation film on the first substrate,
wherein the protective layer is disposed between the gate insulation film and the ink-jet color filter.